

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors:

Craft et al.

Ser. No:

10/706,398

Filing Date:

November 12, 2003

Examiner:

Unknown

Atty. Docket No:

ALA-007B

GAU:

Unknown

For:

PROTOCOL PROCESSING STACK FOR USE WITH INTELLIGENT

NETWORK INTERFACE DEVICE

February 22, 2005

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

2nd Supplemental Information Disclosure Statement

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97(d) and 1.98, applicants bring sixteen documents to the Examiner's attention. Included are copies of nine non-patent reference documents, and a one-page form PTO-1449 listing these documents separately from seven U.S. Patent reference documents. Copies of the seven U.S. Patent reference documents are not enclosed.

Citation of these documents shall not be construed as an admission that the documents are prior art with respect to the instant invention, a representation that a search has been made, or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

Respectfully submitted,

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on February 22, 2005.

Date: 7-22-05

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U.S. Department of Commerce, Patent and Trademark Office					Application No.: 10/706,398			
2 nd SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY					Filing date: November 12, 2003			
APPLICANT						Inventors: Craft et al.		
					Group Art Unit: unknown			
NETWORK INTERFACE DEVICE					Examiner name: unknown			
	o a more with					Attorney Docket No.: ALA-007B		
FEB 2 5 2005			U.S.	. Patent Documents	-2-	 		
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate	
Innes	Α	5,524,250	6/4/96	Chesson et al.	395	775		
	В	5,619,650	4/8/97	Bach et al.	395	200.01		
	С	5,727,142	3/10/98	Chen	395	181		
	D	5,802,258	9/1/98	Chen	395	182.08		
	Е	5,898,713	4/27/99	Melzer et al.	371	53		
	F	6,021,507	2/1/00	Chen	714	2,		
<u>A</u>	G	6,047,323	4/4/00	Krause	709	227		
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•		OTHER	ART—NON PA	ATENT LITERATURE D	OCUMENT	S		
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	1	Schwaderer et al., IEEE Computer Society Press publication entitled, "XTP in VLSI Protocol Decomposition for ASIC Implementation", from 15 th Conference on Local Computer Networks, 5 pages, Sept. 30 – Oct. 3, 1990.						
	2	Beach, Bob, IEEE Computer Society Press publication entitled, "UltraNet: An Architecture for Gigabit Networking", from 15 th Conference on Local Computer Networks, 18 pages, Sept. 30 – Oct. 3, 1990.						
	3	Chesson et al., IEEE Syposium Record entitled, "The Protocol Engine Chipset", from Hot Chips III, 16 pages, Aug. 26-27, 1991.						
	4	Maclean et al., IEEE Global Telecommunications Conference, Globecom '91, presentation entitled, "An Outboard Processor for High Performance Implementation of Transport Layer Protocols", 7 pages, Dec. 2-5, 1991.						
	, 5	Ross et al., IEEE article entitled "FX1000: A high performance single chip Gigabit Ethernet NIC", from Compcon '97 Proceedings, 7 pages, Feb. 23-26, 1997.						
	6	Strayer et al., "Ch. 9: The Protocol Engine" from XTP: The Transfer Protocol, 12 pages, July 1992.						
	7	Publication entitled "Protocol Engine Handbook", 44 pages, Oct. 1990.						
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9 Lilienkamp et al., Publication entitled "Proposed Host-Front End Protocol", 56 pages, Dec. 1984.								
Examiner			Date Considered					
*EXAMINER: I in conformance	nitial if ref e and not	erence considered, considered. Includ	whether or not o e copy of this for	citation is in conformance w m with your communication	ith MPEP 60 to applican	09; Draw line th	nrough citation if not	